EXHIBIT E

GIBSON, DUNN & CRUTCHER LLP

LAWYERS

A REGISTERED LIMITED LIABILITY PARTNERSHIP
INCLUDING PROFESSIONAL CORPORATIONS

333 South Grand Avenue Los Angeles, California 90071-3197
(213) 229-7000
www.gibsondunn.com

SPirnazar@gibsondunn.com

April 6, 2009

VIA ELECTRONIC AND U.S. MAIL

Direct Dial

(213) 229-7444
Fax No.

(213) 229-6444

Client No.

T42376-00764

Bernard Barmann, Esq.
O'Melveny & Myers
400 South Hope Street
Los Angeles, California 90071-2899

Re: *AMD v. Intel — Depositions to be conducted in May and June 2009*

Dear Bernie:

Consistent with our agreed-upon protocols regarding deposition logistics, I write to inform you of the depositions of AMD and third-party witnesses that Intel seeks to take in April, May and June 2009. As we discussed, having reviewed the list of witnesses identified its February 16th letter, Intel is revising the list of the depositions that it expects to take in the coming months.

I.   Depositions of Individual AMD witnesses

A.   *Depositions scheduled for April 2009*

The depositions listed below to be set for May and June are in addition to those that are already scheduled for April and May: Jonathan Smith (April 8); Rob Rivet (April 8-10); Jeff VerHeul (April 9); Doug Hooks (April 10); Joe Menard (April 15); Garrett Beauvais (April 16); Pat Moorhead (April 16-17); Rex Meek (April 17); Imi Mosaheb (April 18); Vlad Rozanovich (April 21); Jerry Sanders (April 21-23); Kurt Hoover (April 24); Carol Chaffee (April 24); Shun Yoshizawa (April 24-25); Tom Schofield (April 30); Bill LaRosa (April 30-May 1); Michael Goddard (April 30-May 1); Ed White (May 7); Roger Pineiro (May 7-8); and John Hamill (May 21).

B.   *Previously noticed depositions still to be scheduled in April, May or June 2009*

In addition to providing notice of the new deposition listed below, Intel also provides revised time estimates for some of the previously noticed depositions that are yet to be scheduled: Ken Morrissey (6 hours); Nathan Nuttall (3 hours); David Somo (7 hours); Pierre-Yves Ferrard (6 hours); John Principe (4 hours); Jeff Gruger (2 hours); Robert Melendres (7 hours); Bharath Rangarajan (7 hours); Kim Little (5 hours); John Williams (12 hours); Harry Wolin (5 hours); Marc Warshawsky (7 hours); Gino Giannotti (4 hours); Adam Tarnowski (3 hours); Karen Guo (5 hours); Michael Russo (4 hours); J.D. Lau (2 hours); Chris Chang (4 hours); Tom Bunch (5 hours); Brett Jacobs (4 hours); James Hunter (4 hours); Fred Sporck

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(3 hours); Gianluca Degliesposti (6 hours); Jochen Polster (4 hours); John Crank (4 hours); Linda Connor (3 hours); Gerhard Zehethofer (4 hours); Sarkar Prabir (2 hours); Gerard Poulizac (2 hours); Sam Rogan (3 hours); Kazuo Sakai (2 hours); Takayuki Kuroshima (2 hours); David Uze (2 hours); Linda Starr (14 hours); Annie Flaig (7 hours); Jeff Venditte (7 hours); David Fionda (7 hours); Tim Wright (14 hours); and Ted Donnelly (7 hours); Alan Wendler (2 hours); Keisuke Matsamoto (2 hours); and Kazuyuki Oji (2 hours). Please provide available dates for these witnesses.

C.   *New depositions noticed for May 2009*

Intel provides notice of the following new depositions in intends to take in May and June:

- Dale Gulick (4 hours);
- Julian Hammersley (2 hours);
- Richard Heye (4 hours);
- Dave Kroll (4 hours);
- Lee Little (3 hours);
- Jon Potter (4 hours);
- Michael Simonoff (4 hours); and
- Dwight Tausz (4 hours);

D.   *Depositions with which Intel will likely not proceed*

At this point, Intel does not expect to go forward with these previously noticed depositions. However, Intel reserves it right to notify AMD and to request that some of these witnesses be scheduled for deposition as a result of the ongoing review of documents and/or the testimony of other witnesses:

- Rick Gayle;
- Mike Leary;
- Aaron Feen;
- Jim Slevin;
- Kris Starr;
- Daryl Sartain;
- Kurt Holman;
- Tem Hall;
- Ed DeSilva;
- Bruce Cieseke;
- Scott White;
- Animesh Mishra;
- Arnold Tobita;
- Michael Soares; and
- Chris Calandro

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II.   <u>**Rule 30(b)(6)**</u> Depositions **and Custodian** of **Records Deposition of ARID**

In addition to the individual AMD witnesses listed above, and the previously noticed Rule 30(b)(6) deposition on cost data (requested for April, yet to be scheduled), Intel also plans to conduct the following Rule 30(b)(6) depositions:

A. <u>Rule 30(b)(6) deposition regarding manufacturing related data</u> – 7 hours. Intel would like to proceed with this deposition on June 4 or 5, or June 12, 2009.

1. The origin, existence, location, and structure of AMD databases.

2. General knowledge of AMD's policies and/or procedures for entering, processing, recording, moving and deleting data in or from AMD databases.

3. The definition and interpretation of data fields, values, abbreviations, and codes in AMD databases.

4. Documents or data maintained or recorded by AMD to inform, describe, or relate to entries made in AMD's databases, or to validate entries for any purpose.

5. The business and operational practices employed by AMD to record data in the AMD databases, including what information is used to determine any subsequent adjustments.

6. The existence of automated or manual linking procedures and other possible interactions among AMD databases.

7. AMD's responses, including the interpretation and clarification of responses, previously produced in response to Intel's questions regarding AMD's databases.

8. The manufacturing data that AMD has produced to Intel in this case.

9. The data and methodology AMD uses to calculate any measure of yield, including but not limited to any yield that AMD has documented in its internal reports or in any external statements.

10.   The data and methodology AMD uses to calculate or forecast the demand for its x86 microprocessors, including but not limited to an explanation of how AMD uses the BOF, *OARS* and any other databases to determine how many x86 microprocessors to manufacture and on what schedule.\

11. For each wafer start, whether meant for commercial sale or for an other purpose such as engineering, development, qualification or distribution of samples, on which AMD x86 microprocessors were manufactured at any point between 1996 and the present:

(a) the facility in which that wafer start was run;

(b) the identity of each type of x86 microprocessor die manufactured, including but not limited to the model name or number, speed and "OPN (or "SKU" or any other identifying information) on that wafer;

(c) the quantity of each type of x86 microprocessor die manufactured on that wafer;

(d) the date(s) on which the wafer was run in the facility;

(e) whether that wafer resulted in a wafer out;

(f) the sort data for each wafer out;

(g) the generation and type of logic process technologies used, at the most detailed level available, in processing that wafer; and

(h) for any wafer start not meant for commercial sale, whether the wafer start was a "full flow" run or a "short flow" run.

12. For each facility in which AMD x86 microprocessors were assembled at any point between 1996 and the present, for each wafer containing x86 microprocessor die received by the facility:

(a) the number of die that are not affixed to a package at any point in time;

(b) for each die not affixed to a package, the identity of the die including but not limited to the model name or number, speed and "OPN" (or "SKU" or any other identifying information);

(c) the number of die that are affixed to a package but never sent to a test facility;

(d) for each die/package combination that is never sent to a test facility, the date of assembly, the identity of the package, and the identity of the die including but not limited to the model name or number, speed and "OPN" (or "SKU" or any other identifying information);

(e) for each die/package combination never sent to a test facility, the final disposition of the combination and the date of such disposition;

(f) the number of die that are affixed to a package and sent to a test facility;

(g) for each die/package combination that is sent to a test facility, the date of assembly, the date on which the combination is sent to a test facility, the identity of the package and the identity of the die including but not limited to the model name or number, speed and "OPN" (or "SKU" or any other identifying information);

13. For each facility in which AMD x86 microprocessors were tested at any point between 1996 and the present, for each x86 microprocessor die/package combination received by the facility:

(a) the dates(s) on which the combination was tested;

(b) whether combination was approved to be marked and packed;

(c) for each combination approved to be marked and packed, the date on which it was marked, the date on which it was packed, how the microprocessor is marked, including but not limited to the model name or number, speed and "OPN" (or "SKU" or any other identifying information);

> (d) for each combination that was not approved to be marked, the final disposition of the combination and the date of such disposition;
>
> (e) for each combination that is not approved to be packed, the final disposition of the combination and the date(s) of such disposition

14. For each microprocessor marked and packed by AMD between 1996 and the present, the identity of each facility to which that microprocessor was shipped until it was sent to a customer, including but not limited to the length of time the microprocessor resided in each facility and the nature or description of each facility.

15. The data and methodology needed to replicate AMD's categorization of items as being part of WIP, die bank, FUM, or finished goods inventory.

16. The data and methodology needed to determine the number of wafer starts, wafer outs, assembly outs, net die per wafer, die to ship, other measures of yield and any metric maintained by AMD in the ordinary course of business regarding AMD's manufacturing, assembling, testing, inventorying or distributing of each of AMD's x86 microprocessor products.

17. AMD's purchase requirements for the tools and equipment necessary for AMD to manufacture its flash and logic products; the purchase details of these tools and equipment; the maintenance schedules and maintenance costs for these tools and equipment; and the purchase lead times for and the life spans of these tools and equipment.

18. The components purchased by AMD for the manufacture of its flash and logic products, including but not limited to wafers, chemicals, packages and package components; and the purchase lead times for and inventories maintained by AMD of these components.

19. The names and positions of AMD employees who would be knowledgeable of the matters listed above, and the departments, groups, or business units within AMD that are responsible or knowledgeable of the matters listed above.

B. Rule 30(b)(6) regarding AMD x86 microprocessors from K5 to Barcelona – 10 hours. Intel will proceed with this deposition during the week of June 1 or June 8, 2009.

1. For each AMD product:

   (a) AMD's initially-planned schedule of tapeout dates, any revisions to that schedule, and the reasons for any revisions;

   (b) the tapeout dates that were communicated to any individual(s) not employed by AMD, any subsequent communications that revised those dates, and the reasons for any such revisions; and

   (c) the actual dates on which tapeouts were completed, and the reasons for any differences between (i) those dates, (ii) the initially-planned or revised tapeout dates, and (iii) any tapeout dates that were communicated to any individual(s) not employed by AMD.

2. For each AMD product:

   (a)   AMD's initially-planned schedule of sample dates and volumes, any revisions to that schedule, and the reasons for any revisions;

   (b)   the sample dates and volumes that were communicated to any individual(s) not employed by AMD, any subsequent communications that revised those dates or volumes, and the reasons for any such revisions; and

   (c)   the actual dates on which samples were completed or delivered to customers or potential customers, and the actual volumes that were completed or delivered, and the reasons for any differences between (i) those dates or volumes, (ii) the initially-planned or revised sample dates or volumes, and (iii) any sample dates or volumes that were communicated to any individual(s) not employed by AMD.

3. For each AMD product:

   (a)   AMD's initially-planned schedule of shipment dates and volumes, any revisions to that schedule, and the reasons for any revisions;

   (b)   the shipment dates and volumes that were communicated to any individual(s) not employed by AMD, any subsequent communications that revised those dates or volumes, and the reasons for any such revisions; and

   (c)   the actual date of shipment for revenue, and the actual volume that was shipped, and the reasons for any differences between (i) the actual date and volume, (ii) the initially-planned or revised dates or volumes, and (iii) any dates or volumes that were communicated to any individual(s) not employed by AMD.

4. For each AMD product:

   (a)   AMD's initially-planned production volume goals, any revisions to those goals, and the reasons for any revisions;

   (b)   the production volume goals that were communicated to any individual(s) not employed by AMD, any subsequent communications that revised those goals, and the reasons for any such revisions; and

   (c)   the actual total volume of production by the dates on which AMD's initially-planned production volume goals were based, and the reasons for any differences between (i) the actual volumes, (ii) the initially-planned or revised volumes, and (iii) any volumes that were communicated to any individual(s) not employed by AMD.

5. For each AMD product:

   (a)   AMD's initially-planned performance features to be incorporated in that product, any revisions to those features, and the reasons for any revisions;

        (b)   the communications to any individual(s) not employed by AMD regarding the performance features to be incorporated in the product, any subsequent communications that revised those features, and the reasons for any such revisions; and

        (c)   the actual performance features incorporated in the product on its initial shipment date, and the reasons for any differences between (a) those features, (b) the initially-planned or revised features, and (c) any features that were communicated to any individual(s) not employed by AMD.

   C.   <u>Rule 30(b)(6) deposition on identification of CPUs included in all computers used by AMD</u> – **3** hours. Intel would like to proceed with this deposition in late May or early June 2006.

      1.   Identification of CPUs (including but not limited to the number of CPUs, the manufacturer of the CPUs, and types of CPUs) included in all workstations, servers, desktop PCs, and laptop PCs used by AMD in its business operations or issued by AMD to its employees from 2000 to present.

   D.   <u>Rule 30(b)(6) deposition on AMD's financial performance, the terms of its offers and deals with OEMs, and the reasons why AMD made or did not make sales</u> - 12 hours. Intel will proceed with this deposition in earl June, 2009.

   E.   As previously noticed, Intel will proceed with the Custodian of Records deposition for AMD's Multimedia Production **– 2** hours-

   F.   In addition, Intel will proceed with Custodian of Records deposition for AMD's production of Analyst and Earnings Call Transcripts – 2 hours.

III.   <u>Scheduling of AMD-witness Depositions</u>

To facilitate efficient scheduling and minimize the need to seek alternate deposition dates, Intel provides the week(s) during which it would like to proceed with each deposition. If no dates in the week(s) listed are not available, please provide all alternate dates in May and June.

   A.   *Depositions to be Scheduled in Late April:*

Please provide available dates in late April for the following witnesses:

- Ken Morrissey (*6* hours – April 20 or 21);
- Nathan Nuttall (3 hours – April 30 or May 1);
- John Principe (4 hours – during the week of April 27);
- Jeff Gruger (2 hours during the week of April 27);
- Pierre-Yves Ferrard (6 hours – during the week of April 27 or May 4);

   B.   *Depositions to be Scheduled in early May:*

Please provide all available dates during the weeks of May 4 or May 11 (unless otherwise noted):

- Robert Melendres (7 hours);

- Bharath Rangarajan (7 hours);
- Kim Little (5 hours);
- David Somo (7 hours);
- Brett Jacobs (2 hours – during the week of May 4 or May 18); .
- James Hunter (2 hours – during the week of May 4 or May 18);
- Gino Giannotti (3 hours – during the week of May 4 or May 18);
- Adam Tarnowski (3 hours – during the week of May 4 or May 18);
- John Williams (12 hours – during the week of May 11 or May 18);
- Marc Warshawsky (7 hours – during the week of May 11 or May 18);
- Julian Hammersley (2 hours – during the week of May 11 or May 18);
- Dale Gulick (4 hours during the week of May 11 or 18);
- Richard Heye (4 hours – during the week of May 11 or 18);
- Gerard Poulizac (2 hours);
- Linda Connor (3 hours);
- Gerhard Zehethofer (4 hours);
- Keisuke Matsamoto (2 hours);
- Kazuyuki Oji (2 hours);
- Custodian of Records deposition for AMD's Multimedia Production (2 hours);

C.   *Depositions to be Scheduled in mid to late May:*

Please provide all available dates during the weeks of May 18 or May 25 (or June 7 if needed):

- Gianluca Degliesposti (6 hours);
- Jochen Polster (4 hours);
- Prabir Sarkar (2 hours);
- Harry Wolin (5 hours – preferably the week of May 25);
- Chris Chang (4 hours);
- Karen Guo (5 hours);
- Michael Russo (4 hours);
- J.D. Lau (2 hours);
- Anne Flaig (7 hours);
- Jeff Venditte (7 hours);
- Lee Little (3 hours);
- Jon Potter (4 hours);
- Tom Bunch (5 hours – during the week of May 25);

- Michael Simonoff (4 hours);
- Dave Kroll (2 hours);
- Fred Sporck (3 hours);
- John Crank (4 hours);
- Sam Rogan (3 hours);
- Kazuo Sakai (2 hours);
- Takayuki Kuroshima (2 hours);
- David Uze (2 hours);
- Rule 30(b)(6) deposition on identification of CPUs included in all computers used by AMD or issued by AMD to its employees (3 hours);
- Custodian of Records deposition for AMD's production of Analyst and Earnings Call Transcripts (2 hours);

D. *Depositions to be Scheduled in Early June:*

Please provide all available dates during the weeks of June 1 or June 8:

- Linda Starr (14 hours);
- David Fionda (7 hours);
- Ted Donnelly (7 hours);
- Dwight Tausz (4 hours);
- Tim Wright (14 hours — the week June 8);
- Alan Wendler (2 hours);
- Rule 30(b)(6) deposition on AMD x86 microprocessors from K5 to Barcelona (10 hours).
- Rule 30(b)(6) deposition on manufacturing related data – (7 hours, June 4, 5 or 12).
- Rule 30(b)(6) deposition on AMD's financial performance, the terms of its offers and deals with OEMs, and the reasons why AMD made or did not make sales (12 hours).

E. *Specific availability of Intel Teams to conduct AMD Witness Depositions:*

Team 1 —will be taking the depositions of David Somo, Robert Melendres, Bharath Rangarajan, Kim Little, John Hamill, Chris Chang, Linda Starr, Annie Flaig, Jeff Venditte, Michael Russo, Karen Guo, J.D. Lau, Lee Little, Jon Potter, and the Rule 30(b)(6) deposition on AMD x86 microprocessors. Except for May 14-15 and May 21 (Hamill) this team is able to schedule depositions on any dates in May and June.

Team 2 – will be taking the depositions of John Williams, Marc Warshawsky, Harry Wolin, and Tim Wright. With the exception of May 4-5, May 7-8, and June 3-5, this team is able to schedule depositions on any other dates in May and June.

Team 3 – will be taking the depositions of Ken Morrissey, Michael Goddard, Roger Pineiro, Tom Bunch, Richard Heye, Julian Hammersley, Dale Gulick, and the Rule 30(b)(6) deposition on manufacturing related data. At this time, with the exception of May 1, May 7-8, May 15, June 1-2 and June 9-10, this team is available to schedule depositions on any other dates in May or June.

Team 4 – will be taking the depositions of Adam Tarnowski, Gino Gionnotti, and the Rule 30(b)(6) deposition on identification of CPUs included in all computers used by AMD or issued by AMD to its employees. Other that May 11 to 15, this team is available to schedule depositions on any dates in May or June.

Team 5 – will be taking the depositions of Nathan Nuttall, Ed White (May 7), James Hunter, Gianluca Degliesposti, Jochen Polster, Prabir Sarkar, and David Kroll. Except for May 11 to 15 and May 18, this team is available to schedule depositions on any May or June dates.

Team 6 – will be taking the depositions of Brett Jacobs, Linda Connor, Gerhard Zehethofer, Fred Sporck, John Crank. At this point, it appears that this team is available to schedule depositions on any May or June dates.

Team 7 – will be taking the deposition of John Principe, Jeff Gruger, Ted Donnelly, and Dwight Tausz. Intel requested late April dates for John Principe and Jeff Gruger. AMD has indicated that it may not be able to offer these witnesses during April. Due to prior travel commitments for much of May, this team can proceed with these deposition on dates in June.

Team 8 – will be taking the deposition of David Fionda. Due to medical constraints, this team can only proceed with this deposition at the of May or in June.

Other teams – will be taking the depositions of Pierre-Yves Ferrard, Alan Wendler, Michael Simonoff, Gerard Poulizac, Sam Rogan, Kazuo Sakai, Takayuki Kuroshima; David Uze; Keisuke Matsamoto; Kazuyuki Oji. These teams are generally available to schedule depositions on dates in May and June.

IV. Deposition of Third Party Witnesses

A. *Newly noticed depositions and revisions to previously noticed depositions:*

Intel provides notice of the following new third-party depositions it will seek to take in late May and early June:

1. Jim Tallman – 6 hours
2. David Kulbarsh – 6 hours
3. Marie Crane – 9 hours
4. Reed Holden – 8 hours
5. Tom Krazit (CNET New) – 1 hour;
6. Don Clark (Wall Street Journal) – 1 hour;
7. Ken Popovich (eWeek) – 1 hour;

8. Lisa DiCarlo (eWeek) – 1 hour;

9. Michael Miller (PCMag.com) – 1 hour;

10. Robert Mullins (computerworld.com) – 1 hour;

11. Custodian of Records of Tallman insights – 3 hours;

12. Custodian of Records of SicolaMartin – **3** hours;

13. Custodian of Records of Kulbarsh Consulting – 3 hours;

14. Custodian of Records of M. Crane & Associates – 3 hours;

15. Custodian of Records of McKinsey & Co. – 4 hours;

16. Custodian of Records for CallStreet – 2 hours;

17. Custodian of Records for Thomson StreetEvents (aka CCBN StreetEvents) – 2 hours;

18. Custodian of Records for Fair Disclosure Financial Network (FDFN) – **2** hours;

19. Custodian of Records for Seeking Alpha – **2** hours;

20-25. <u>Rule 30(b)(6) depositions of the following Retailers:</u> Fry's Electronics (2 hours); Office Depot (**2** hours); Staples (**2** hours); MEI (2 hours); Office Max (2 hours); and CDW Corp. (2 hours). **A** preliminary list of topics of examination follows:

1. Your marketing and pricing strategies for Computer Products in any of the following segments: retail/consumer; small/medium business; corporate; or other customer groups to which you sell.

2. The factors that You consider in pricing of Computer Products.

3. The relationship in retail pricing between Computer Products containing AMD processors and those containing Intel processors.

4. A description, target, and duration of any types of price promotion programs that You offered regarding the sale of Computer Products.

5. Your strategic analyses or plans or competitive reviews regarding the retail market for Computer Products.

6. Any analyses or consideration that You gave to differential pricing of Computer Products, whether geographical, platform- or business segment-based.

7. The time fag between the announcement of a change in the cost of a microprocessor and a change in the price of Your Computer Products containing the processor.

8. How frequently (daily, weekly, monthly) sales prices for Your Computer Products change, whether sales prices vary from store to store and why, and whether store managers have the discretion to change the sales price.

9. The use of any type of financial incentive provided to You by either AMD or Intel.

10. The geographic locations of any business dealings between You and Intel.

11. The extent and manner of Your dealings with any ODMs or manufacturers of Computer Products you purchased or contemplated purchasing to self unbranded or under Your own private label.

12. Any allegations in the Class or AMD complaints concerning You.

26. <u>Rule 30(b)(6) deposition of Best Buy regarding the Best Buy Value Equation</u> – 3 hours

27. <u>Rule 30(b)(6) deposition of Gartner Griggs-Anderson regarding Wave Tracking Surveys for AMD</u> – **3** hours

28. <u>Rule 30(b)(6) deposition of Glover Park</u> – 5 hours

1. Any Services provided by Glover Park at the request of, on behalf of, or related to AMD from July 1, 2004 through February 28, 2005, including without limitation Services[1] related to the following subject matter:

a. Intel;

b. Project Slingshot; and

c. "Fair and open competition" in the market for x86 Microprocessors, as referenced during the deposition of Ms. Beth Ozmun on March 6, 2009.

d. The program or project referred to as "Break Free".

2. Glover Park's knowledge of any litigation proposed or contemplated by AMD against Intel.

B. *Previously Noticed Depositions of Third Party witnesses*

For the sake of completeness, I have included Intel's time estimates and counter-designations for previously noticed depositions of third-party witnesses:

1. Rollins, Kevin – **3** hours
2. Beyers, Joe – 4 hours
3. Callaway, Jeri – 4 hours
4. Mouton, James – 3 hours
5. Lee, Jo – **3** hours
6. Robison, Shane – 5 hours
7. Winkler, Michael – 4 hours
8. Stallard, Scott – 4 hours
9. Clark, Ted – 4 hours
10. Fiorina, Carly – **3** hours
11. Callahan, Ted – 3 hours
12. Cador, Eric – **3** hours
13. Zeitler, William – 5 hours
14. Adkins, Rod – **3** hours
15. Gupta, Satish – 3 hours
16. Rasmussen, Dave – **3** hours
17. O'Sullivan, Fran – **3** hours

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[1] The term "Service" means any work related to public relations, media relations, lobbying, strategic messaging, corporate communications, advocacy and/or focus groups.

18. Ward, Steve – 4 hours
19. Schmoock, David – 2 hours
20. Davy, Phillippe – 4 hours
21. Hortensius, Peter – 4 hours
22. Inouye, Wayne – 4 hours
23. Waitt, Ted – 4 hours
24. Klassen, Chris – 3 hours
25. Booth, James – 3 hours
26. Memo, Greg – 3 hours
27. May, Chuck – 3 hours
28. Koester, Dean – 2 hours
29. Ford, Todd – 3 hours
30. Barton, Tom – 3 hours
31. McLauhlin, Maria – 2 hours
32. Stouder, Bret – 2 hours
33. Adkins, Michael – 2 hours
34. Manca, Pete – 2 hours
35. Sparchman, Ben – 2 hours
36. Boyle, Rick – 2 hours
37. Bennett, Chris – 2 hours
38. Ichinaga, Stephen – 2 hours
39. Speirkel, Gregory – 2 hour
40. Bradley, Keith – 2 hour
41. Huang, Robert – 2 hour
42. Witness TBD (FutureMark) – 2 hours
43. Hanson, Mark (Sony) – 2 hours;
44. Barney, Jeff (Toshiba) – 2 hours;
45. Simons, Mark (Toshiba) – 2 hours;
46. Poole, Will (Microsoft) – 2 hours;
47. Russell, Richard (Microsoft) – 2 hours;
48. Srinivasan, Rajesh (Microsoft) – 2 hours;
49. Valentine, Brian (Microsoft) – 2 hours;
50. Cutler, Dave (Microsoft) – 2 hours;
51. Tibbils, Kent (ASI) – 2 hours;
52. Anne, Seshu (Dell) – 2 hours;
53. Neland, Glenn @ell) – 2 hours;
54. 30(b)(6) re Dell Rebates – 2 hours
55. 30(b)(6) of NEC Corp. – 4 hours
56. 30(b)(6) of NEC Corp. of America – 4 hours
57. 30(b)(6) of Fujitsu Computer Systems – 4 hours
58. 30(b)(6) of Fujitsu Ltd – 4 hours
59. 30(b)(6) of Sony Electronics – 4 hours
60. 30(b)(6) of Sony Corp. – 4 hours
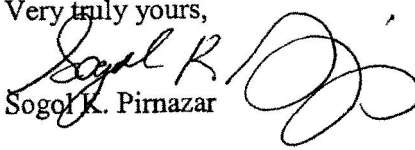61. 30(b)(6) of Toshiba America Info. Sys – 4 hours

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       62. 30(b)(6) of Acer America –2 hours

       63. Weil Gotshal - Custodian of Records – 1 hour

As always, please let me know if you have **any** questions.

          Very truly yours,

          Sogol K. Pirnazar

SKP/skp
100632217_1.DOC
cc: Daniel S. Floyd, Esq.
       Mark E. Weber, Esq.